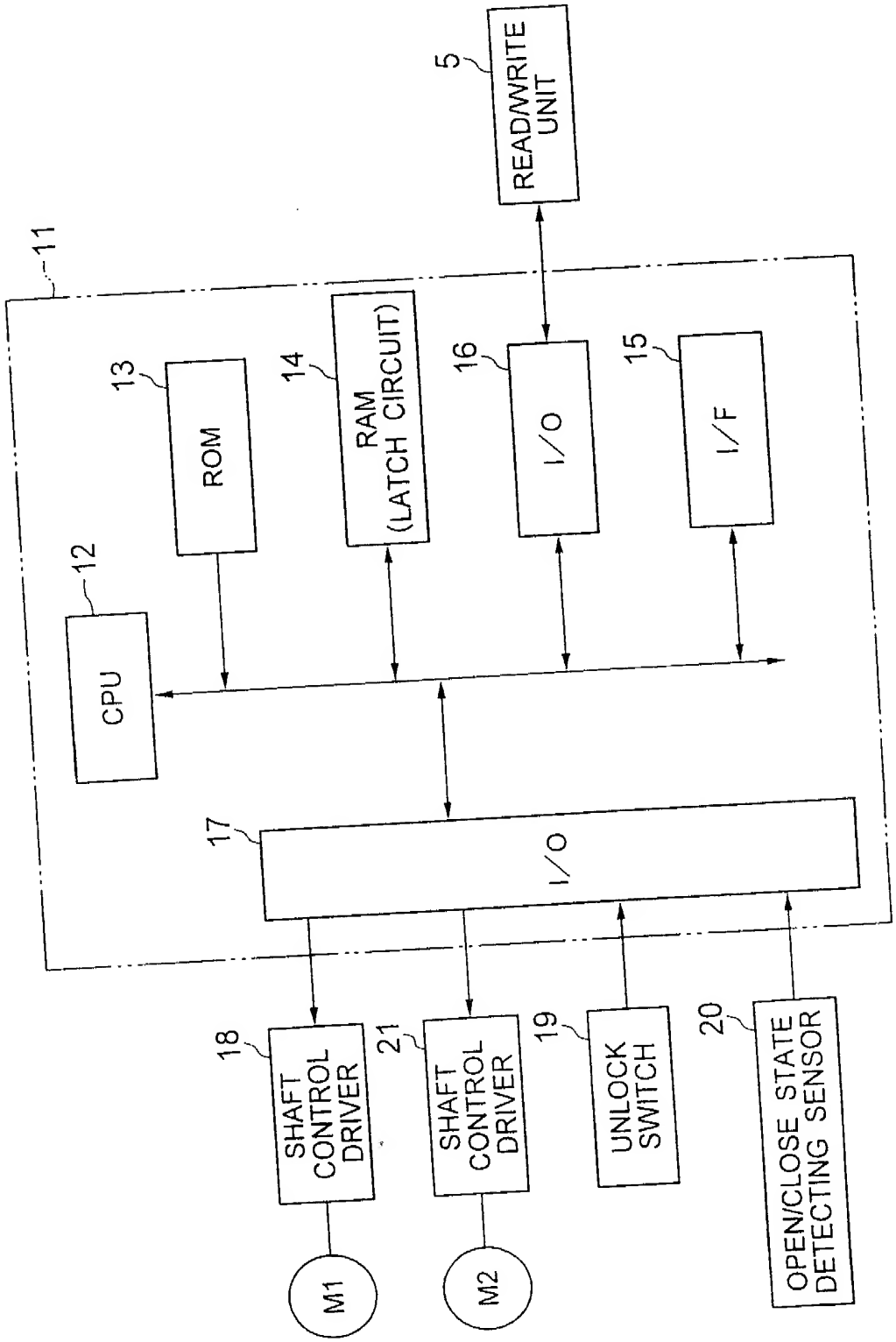
FIG. 4


5
READ/WRITE UNIT
11
13
ROM
14
RAM (LATCH CIRCUIT)
16
I/O
15
I/F
12
CPU
17
I/O
18
SHAFT CONTROL DRIVER
21
SHAFT CONTROL DRIVER
19
UNLOCK SWITCH
20
OPEN/CLOSE STATE DETECTING SENSOR
M1
M2